FIG 1

()

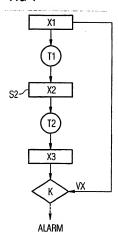
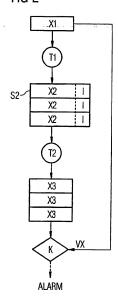
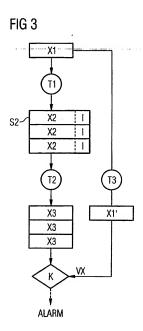


FIG 2

()



()



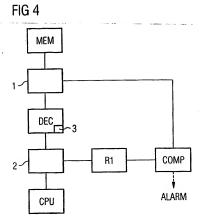


FIG 5

MEM

DEC

3

R1

COMP

ALARM

CPU